

IR Receiver for Data Communication

Description

The IC U2538B is a complete IR receiver for data communication. The useful input signals are separated by a special input circuit and amplified by a gain-controlled amplifier. The bandpass filter suppresses the off-band signals. The signal detector, consisting of a demodulator,

an integrator and a Schmitt Trigger, forms the input signal to an output pulse that can be interfaced to a microcomputer. The AGC and the ATC circuit control the receiver's sensitivity, making it insensitive to ambient light sources.

Features

- Few external components
- Low power consumption
- Microcomputer compatible
- Insensitive to ambient light and other continuous interference

Applications

- Keyless entry systems
- Remote control
- Wireless data transfer up to 4 kbit/s

Case: SO8 U2538B-FP

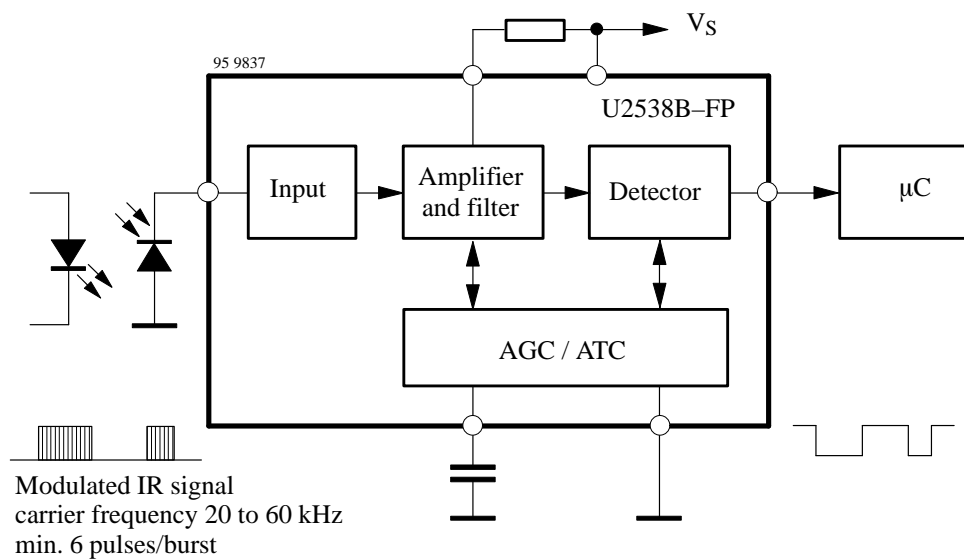


Figure 1.

Pinning

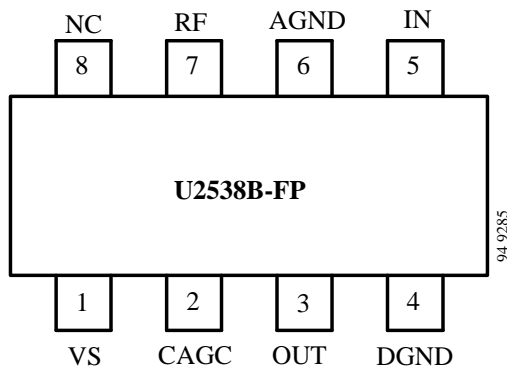


Figure 2.

Pin Description

Pin	Symbol	Function
1	VS	Supply voltage
2	CAGC	AGC capacitor
3	OUT	Data output
4	DGND	GND – DEM/INT/ST
5	IN	Input pin diode
6	AGND	GND amplifier
7	RF	Frequency determination
8	NC	Not connected

Block Diagram

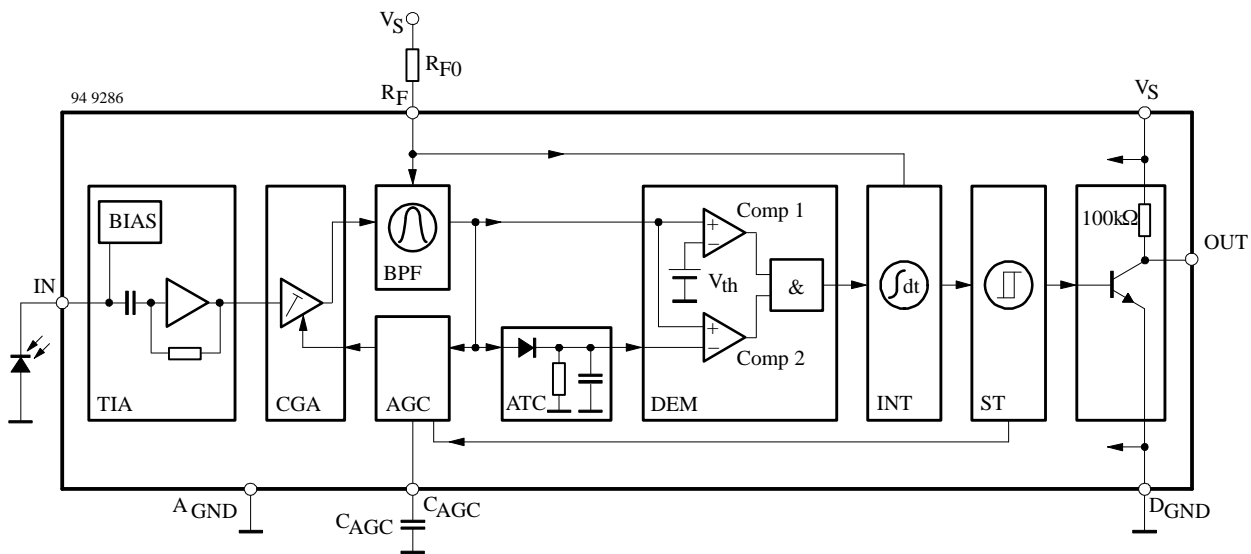


Figure 3.

TIA Trans impedance amplifier
CGA Controlled-gain amplifier
BPF Bandpass filter
AGC Automatic gain control

ATC Automatic threshold control
DEM Demodulator
INT Integrator
ST Schmitt Trigger

Functional Description

Input Stage (TIA)

The input stage provides the necessary bias voltage for the photo diode and ensures decoupling of the useful signal. This involves processing the dc and ac portions in separate parts of the circuit: the bias voltage (BIAS) and the transimpedance amplifier circuit (TIA). The bias voltage circuit operates like a load resistor with respect to the photo diode, the value of which is low for dc and low-frequency signals (3 to 100 kΩ) but as high as possible for the operating frequency (100 kΩ to 1 MΩ) depending on the input current) The ac portion of the input signal feeds an inverted amplifier with a sufficiently low input resistance ($Z_i < 10 \text{ k}\Omega$). If the input resistance were too high, the useful signal would be lost to the junction capacitance of the photo diode.

Gain Controlled Amplifier (CGA)

The gain controlled amplifier accounts for the greatest part of the voltage gain and can be controlled via the voltage at C_{AGC} (pin 2). Gain control is needed to support the interference suppression of the detector. High-pass behaviour results from the capacitive coupling of the individual stages. The cut-off frequency is approximately 20 kHz.

Bandpass Filter (BPF)

The bandpass filter is largely made up of integrated components. An external resistor determines the mid-frequency. The filter quality is about 7 and is practically independent of the selected mid-frequency (see figure 4). The following formula can be used for calculating the resistor, R_{f0} :

$$R_{f0} \text{ (k}\Omega\text{)} = \frac{8855}{f_0 \text{ (kHz)}} - 13$$

where: $20 \text{ kHz} < f_0 < 60 \text{ kHz}$

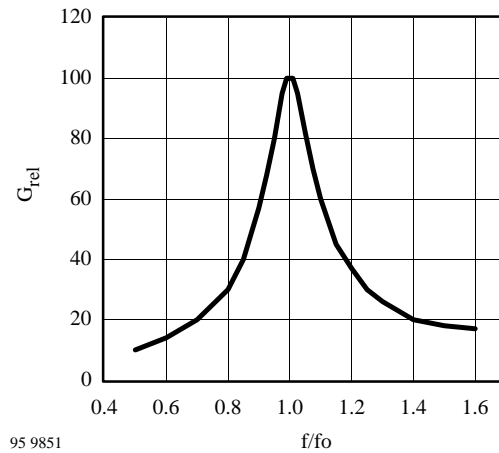


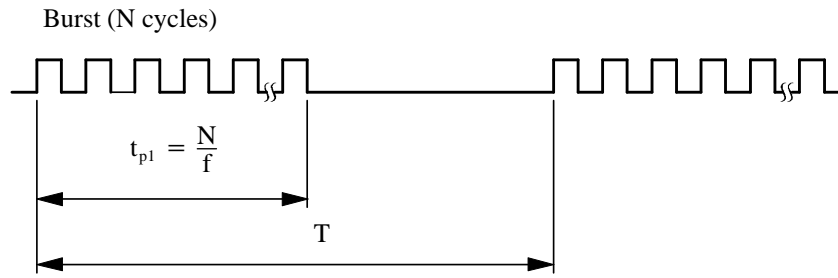
Figure 4. Characteristic of the bandpass filter

Automatic Threshold Control (ATC)

During the reception of an incoming telegram, the ATC reduces the sensitivity of the demodulator to establish the highest possible signal to noise ratio according to the signal strength. This prevents interferences, which may arise during the transmission from affecting the output. The advantage of the circuit is attained, if its output voltage exceeds V_{Th} (Comp 1). That is the case, when the input signal strength is more than double of the minimum detectable signal intensity.

Automatic Gain Control (AGC)

The automatic gain control improves the circuit's resistance to interference by adapting the amplification of the gain controlled amplifier to the relevant existing interference level. In order to prevent the circuit from responding to transmitted data signals, it gradually reduces the sensitivity but only if the duty cycle exceeds a specific value (see figure 5). When using telegrams with higher duty cycles than this value, the capacitor, C_{AGC} , maintains the sensitivity for a certain time period. A higher capacitance enables a longer transmission time. A capacitance of $C1 = 22 \text{ nF}$ is adequate for most known telegrams. A typical value for the maximum duty cycle (DC) can be calculated by the following formula:



$$DC = \frac{t_{p1}}{T} \quad N \geq 6 \quad f = 20 \text{ to } 60 \text{ kHz}$$

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Figure 5.

Detector

The output signal of the bandpass filter is compared to a fixed reference (Comp 1) and to a reference generated by the ATC circuit (Comp 2). The output of the comparator with the higher threshold voltage controls the integrator. Use of the integrator keeps the output free of short-time interference.

The integrator drives the output stage after being pro-

cessed through a Schmitt Trigger. The internal pull-up resistor can replace an external resistor, in some applications.

$$DC_{max} = \frac{N}{14.2 + 1.1 N}$$

Absolute Maximum Ratings

Parameters	Symbol	Value	Unit
Supply voltage Pin 1	V_S	-0.3 to +6.0	V
Input voltages Pins 2, 3 and 5	V_{IN}	-0.3 to V_S	V
Pin 7		-0.3 to +1.5	
Input current Pin 7	I_{IN}	0 to 0.1	mA
Power dissipation $T_{amb} = 105^\circ\text{C}$	P_{tot}	110	mW
Junction temperature	T_j	125	$^\circ\text{C}$
Ambient temperature	T_{amb}	-40 to +105	$^\circ\text{C}$
Storage temperature	T_{stg}	-40 to +125	$^\circ\text{C}$

Maximum Thermal Resistance

Parameters	Symbol	Maximum	Unit
Junction ambient	R_{thJA}	180	K/W

Electrical Characteristics

$T_{amb} = 25^\circ\text{C}$, $V_S = 5 \text{ V}$

Parameters	Test Conditions / Pins	Symbol	Min.	Typ.	Max.	Unit
Supply voltage	Pin 1	V_S	4.5		5.5	V
Supply current	Pin 1	I_S	0.35		0.65	mA
Max. input current $V_{IN} = 0$	Pin 5	I_{IN}	0.6			mA
Output voltage low: $I_{OL} = 2 \text{ mA}$	Pin 3	V_{OL}			0.2	V
Internal pull up resistor	Pin 3	R_L	75	100	125	k Ω
Center frequency of band-pass RF = 240 k		f_0	33.3	35	36.7	kHz

Parameters	Test Conditions / Pins	Symbol	Min.	Typ.	Max.	Unit
Q factor		Q		7		
Frequency range		f	20		60	kHz
AGC current source sink	Pin 2		90 70	120 100	155 140	nA nA
AGC slope	Pin 2			20		dB/V
Nr. of pulses required			6			
Sensitivity	Pin 5				0.7	nA(rms)
Switch on delay, $i_{IN} = 0.7 \text{ nA (rms)}$	Pin 3 see figure 6	t_{don}	3		7.5	Period
Switch off delay, $i_{IN} = 0.7 \text{ nA (rms)}$	Pin 3 see figure 6	t_{doff}	5		10	Period
Pulse width, $i_{IN} = 0.7 \text{ nA (rms)}$, 6 pulse burst	Pin 3 see figure 6	t_{po}	4.5		10	Period

$$R_{iO} \text{ (k}\Omega\text{)} = \frac{8855}{f_0 \text{ (kHz)}} - 13 \text{ k}\Omega$$

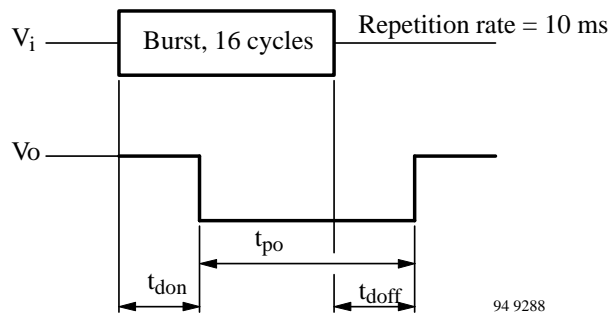
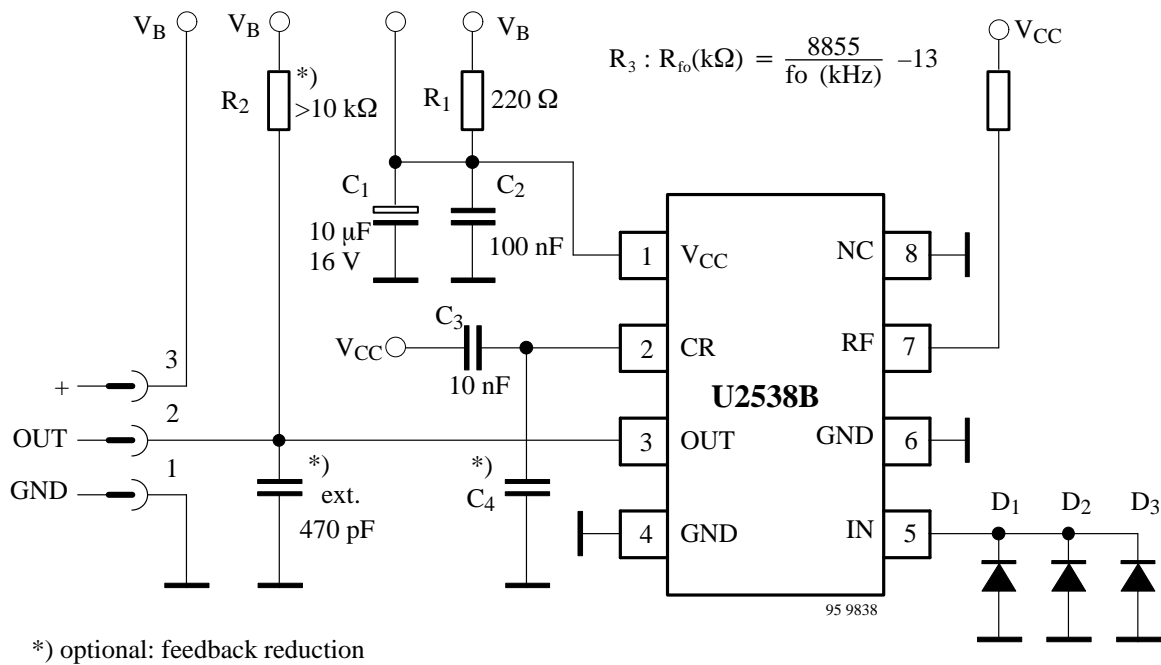


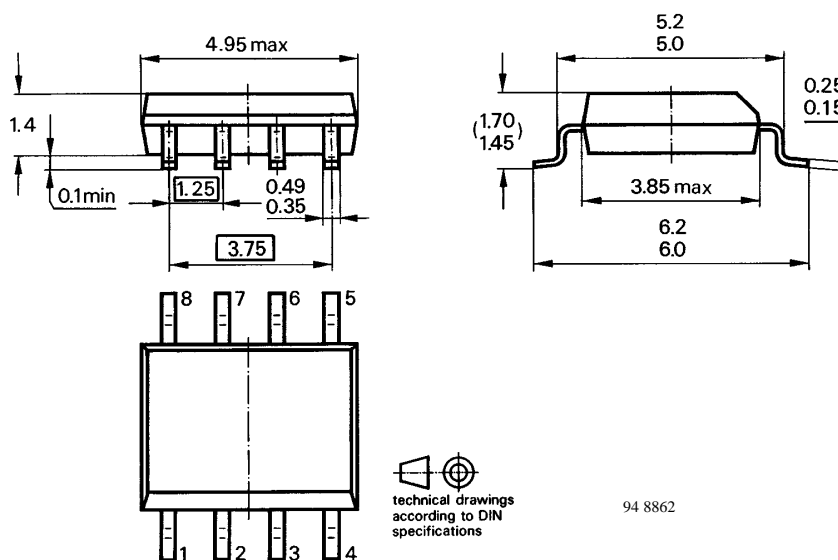
Figure 6.

Application Circuit



Dimensions in mm

Package: SO 8



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2. Regularly and continuously improve the performance of our products, processes, distribution and operating systems with respect to their impact on the health and safety of our employees and the public, as well as their impact on the environment.

It is particular concern to control or eliminate releases of those substances into the atmosphere which are known as ozone depleting substances (ODSs).

The Montreal Protocol (1987) and its London Amendments (1990) intend to severely restrict the use of ODSs and forbid their use within the next ten years. Various national and international initiatives are pressing for an earlier ban on these substances.

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1. Annex A, B and list of transitional substances of the Montreal Protocol and the London Amendments respectively
2. Class I and II ozone depleting substances in the Clean Air Act Amendments of 1990 by the Environmental Protection Agency (EPA) in the USA
3. Council Decision 88/540/EEC and 91/690/EEC Annex A, B and C (transitional substances) respectively.

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